

FIG.1A

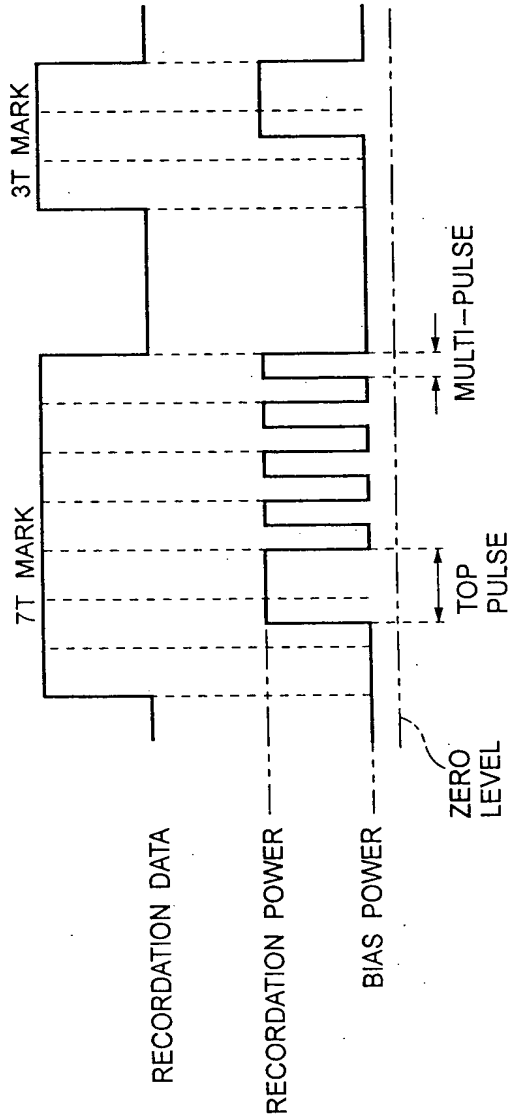


FIG.1B

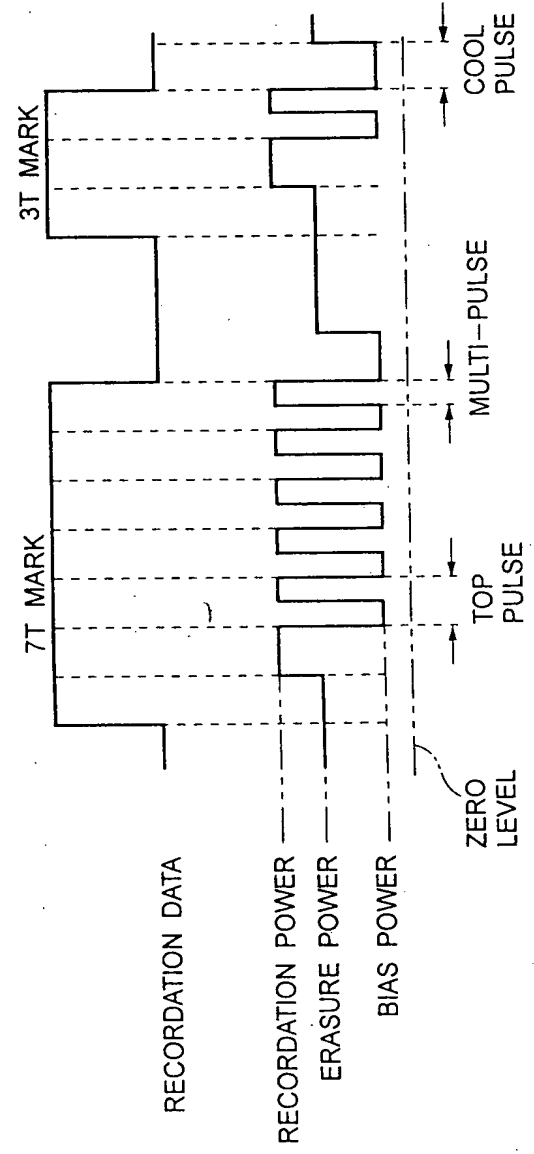


FIG.2A

FIG.2B

FIG.3A

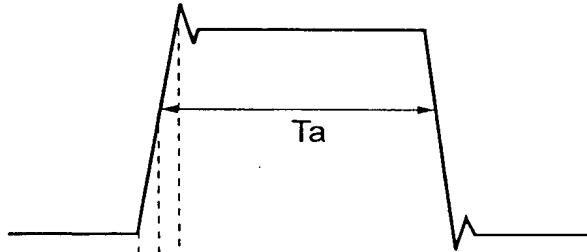


FIG.3B

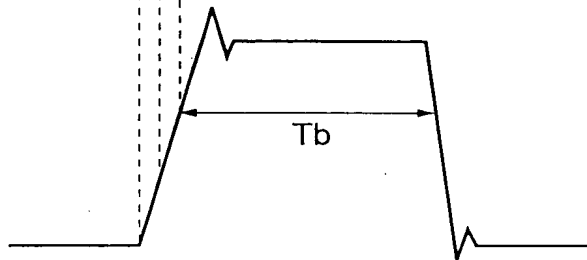


FIG.4A

WRITE PULSE

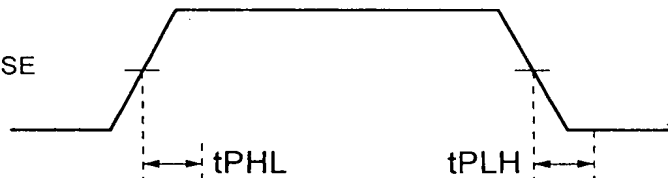


FIG.4B

IN PHASE OUTPUT

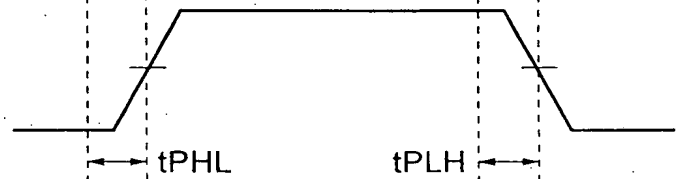
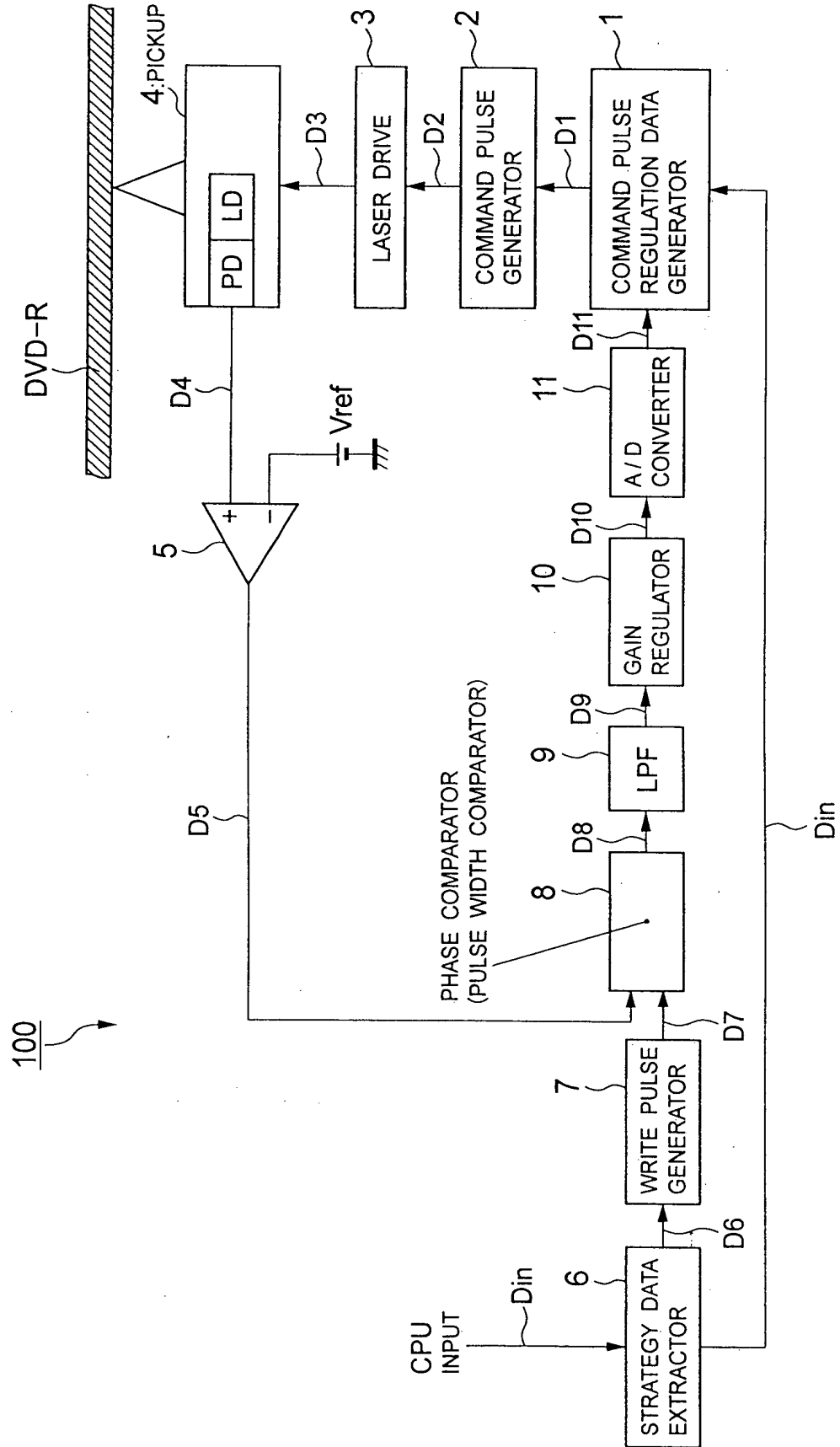


FIG.4C

INVERTED OUTPUT



FIG.5



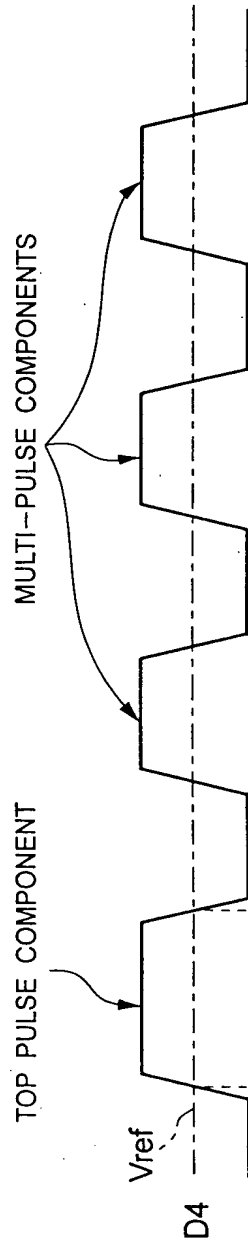


FIG. 6A

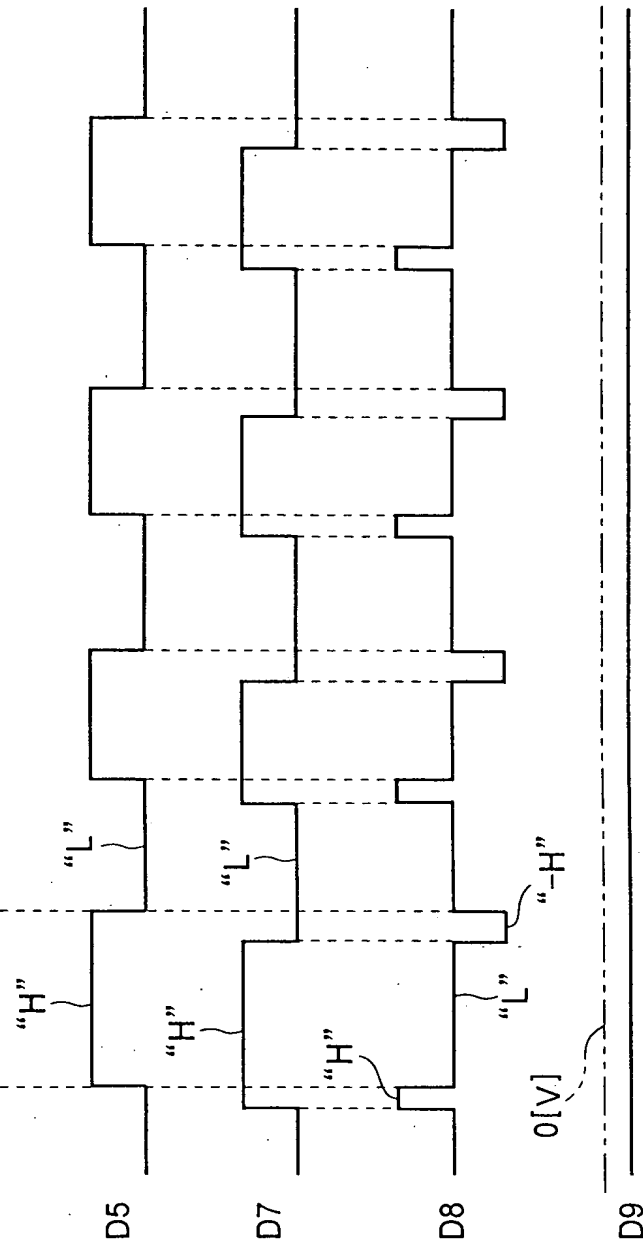


FIG. 6B

FIG. 6C

FIG. 6D

FIG. 6E

TIME

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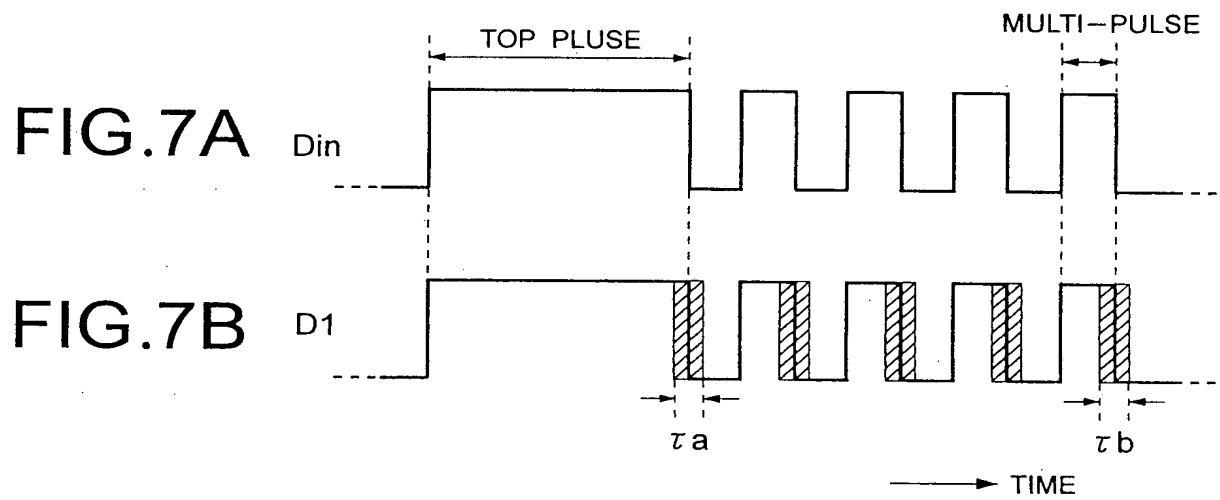
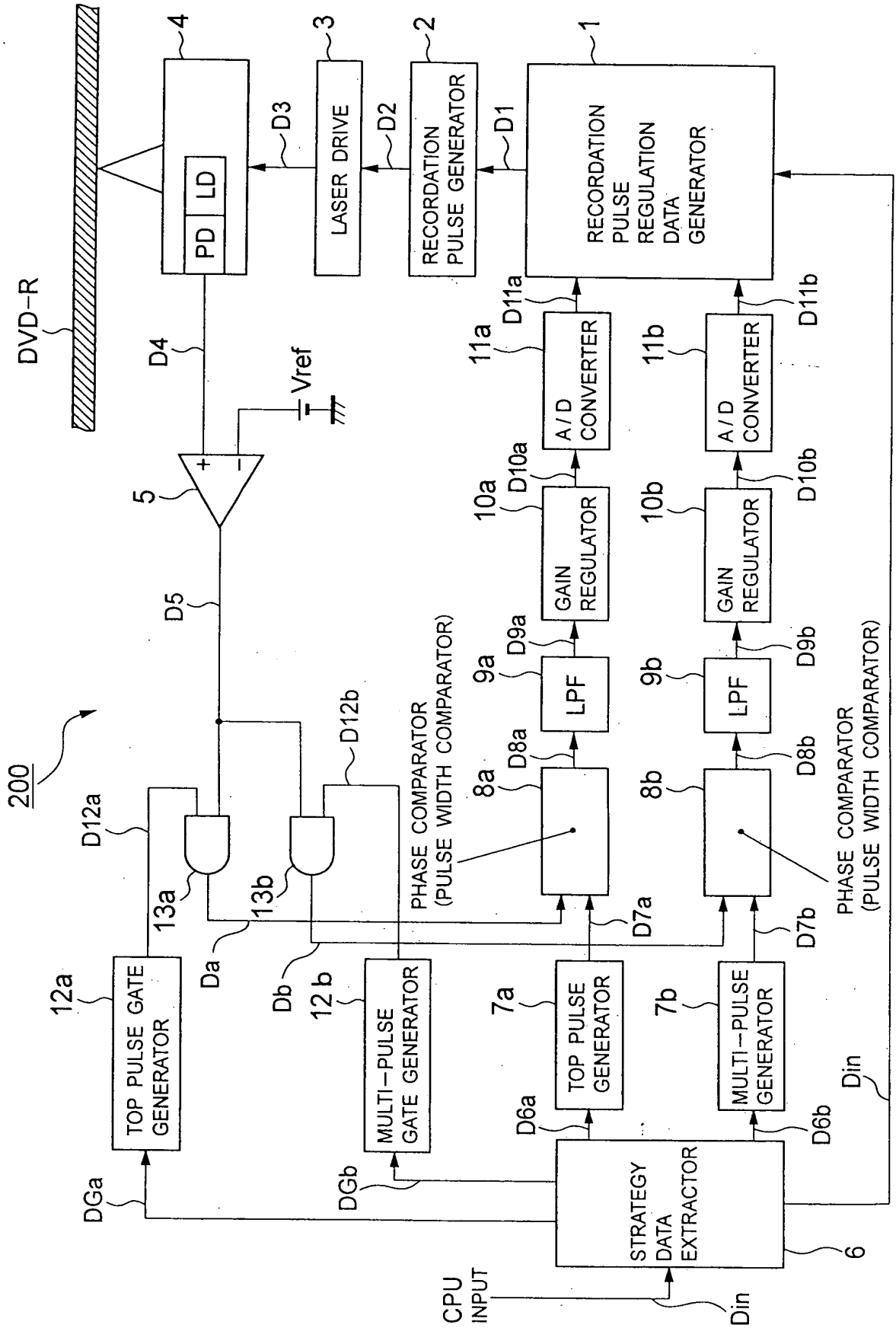
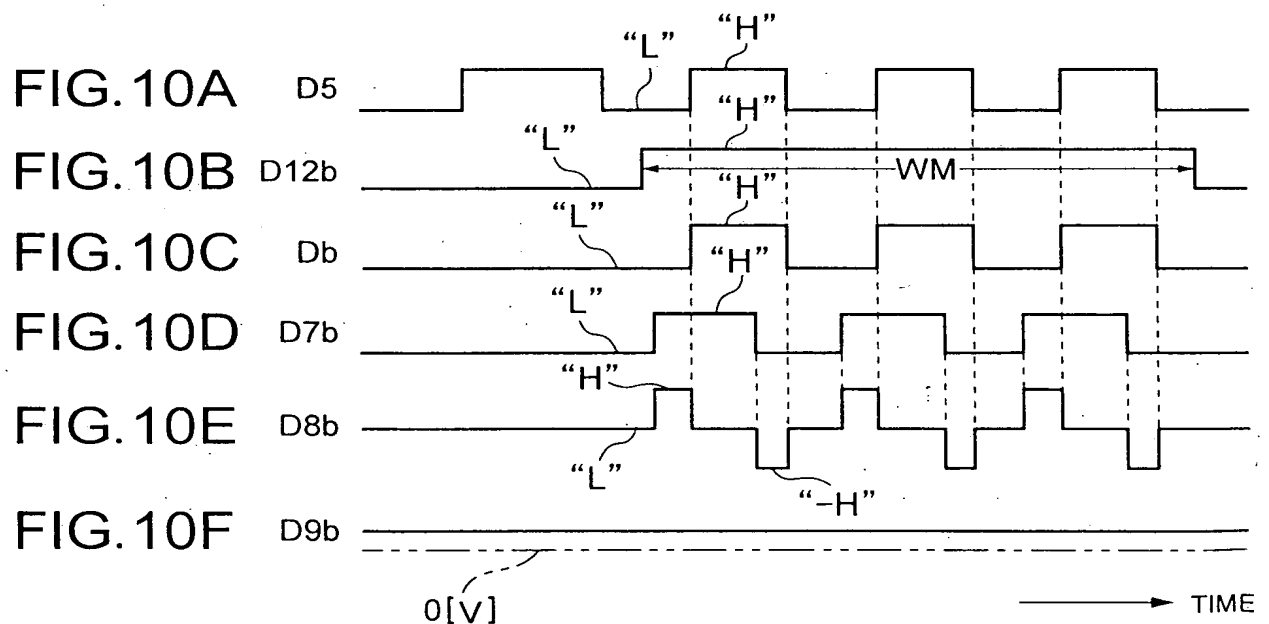
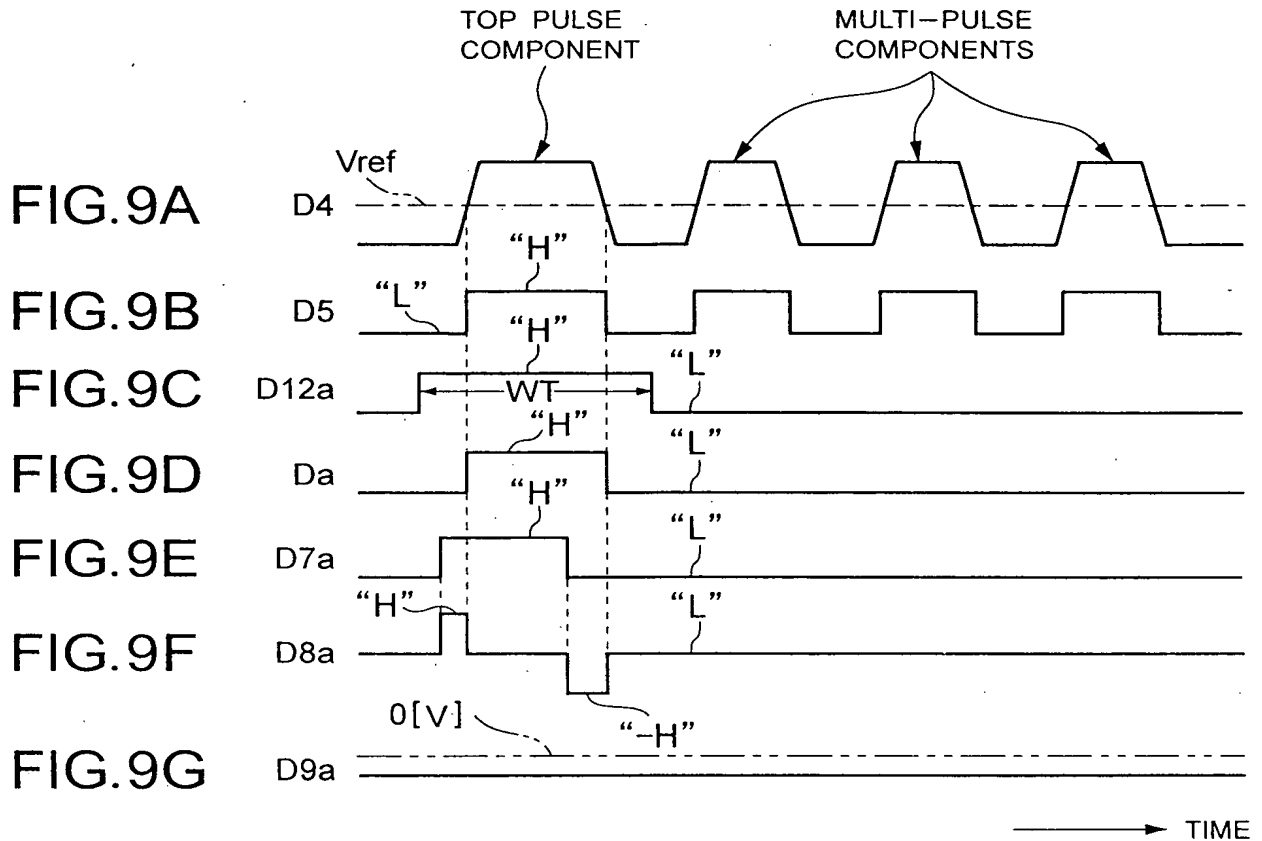
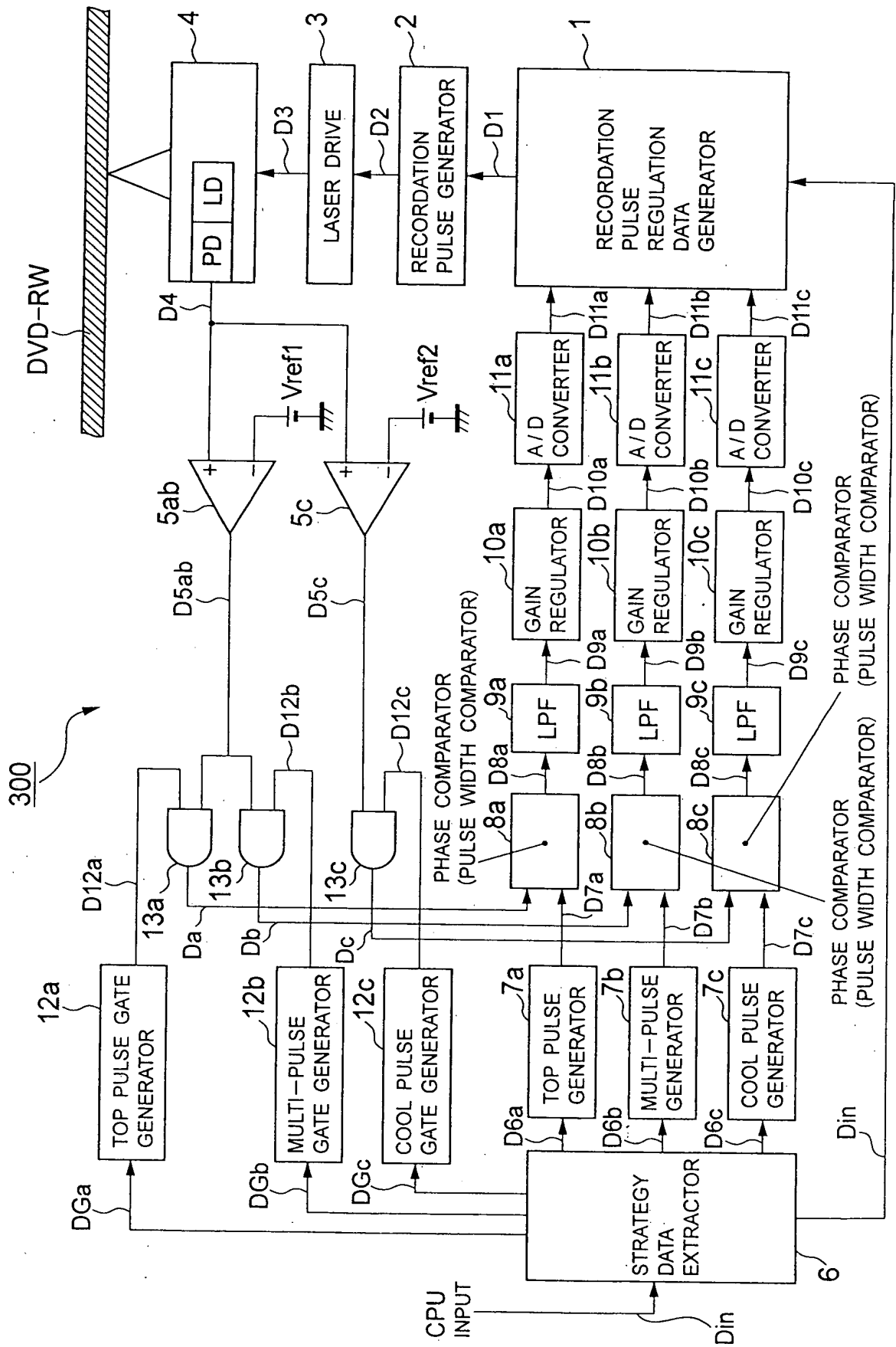


FIG. 8

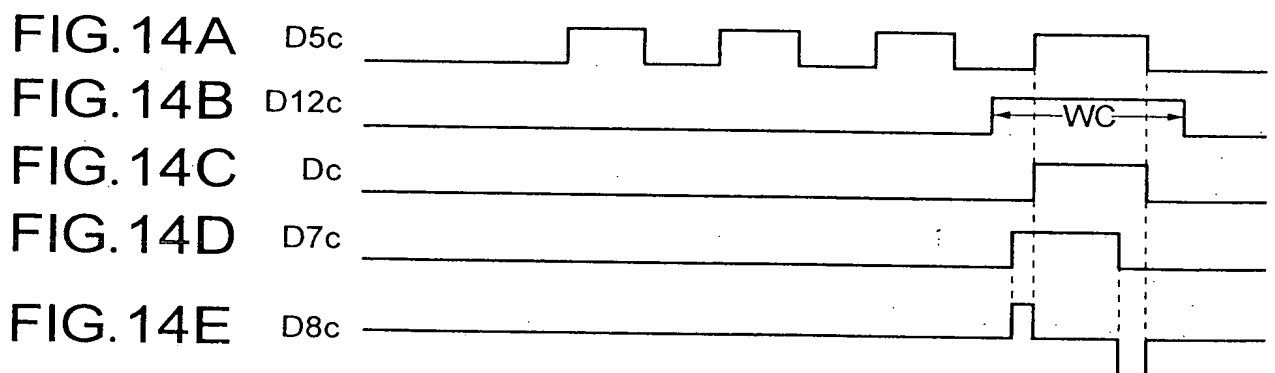
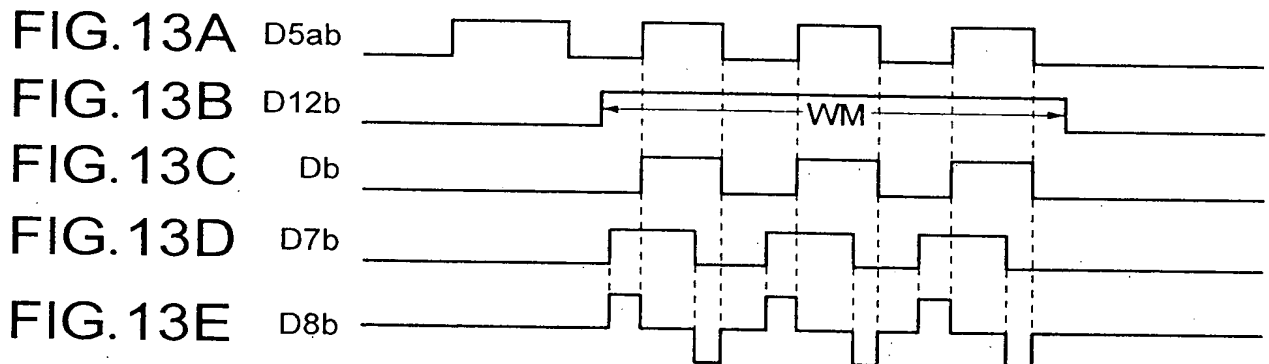
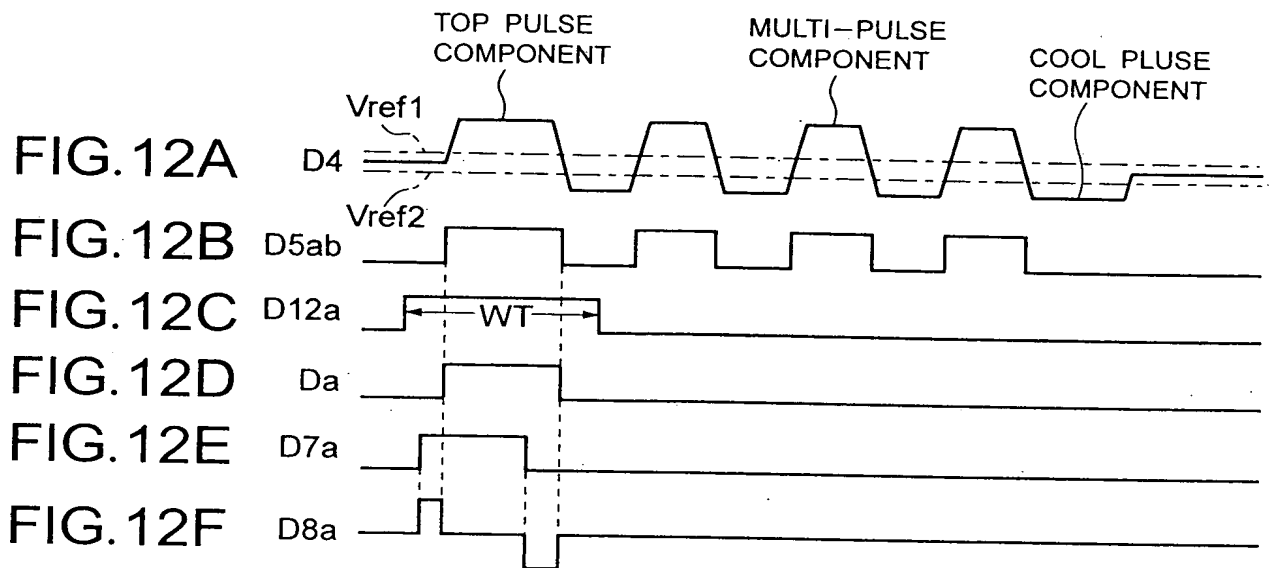




300



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→ TIME

FIG.15A

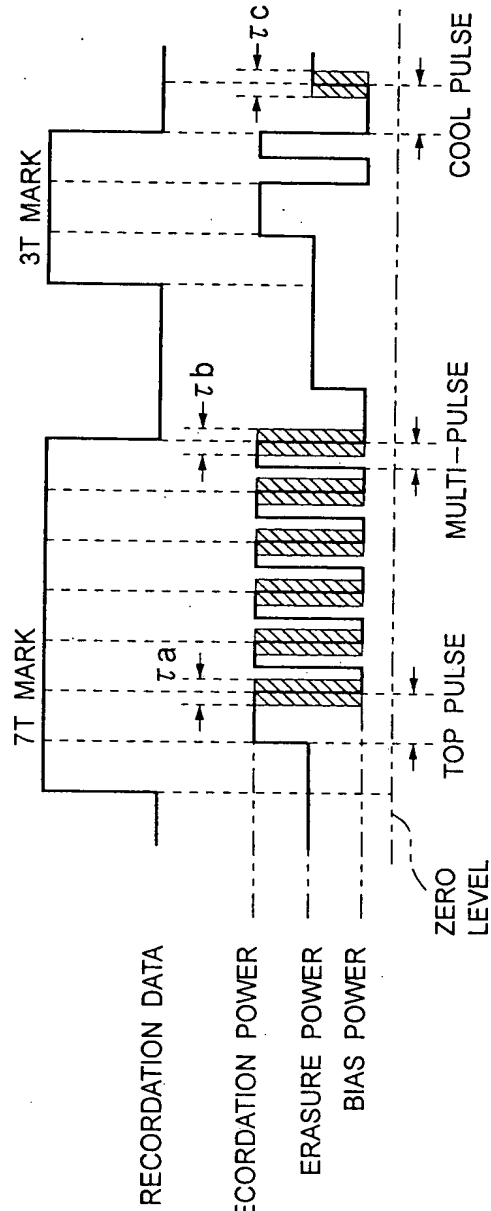


FIG.15B

FIG.16

